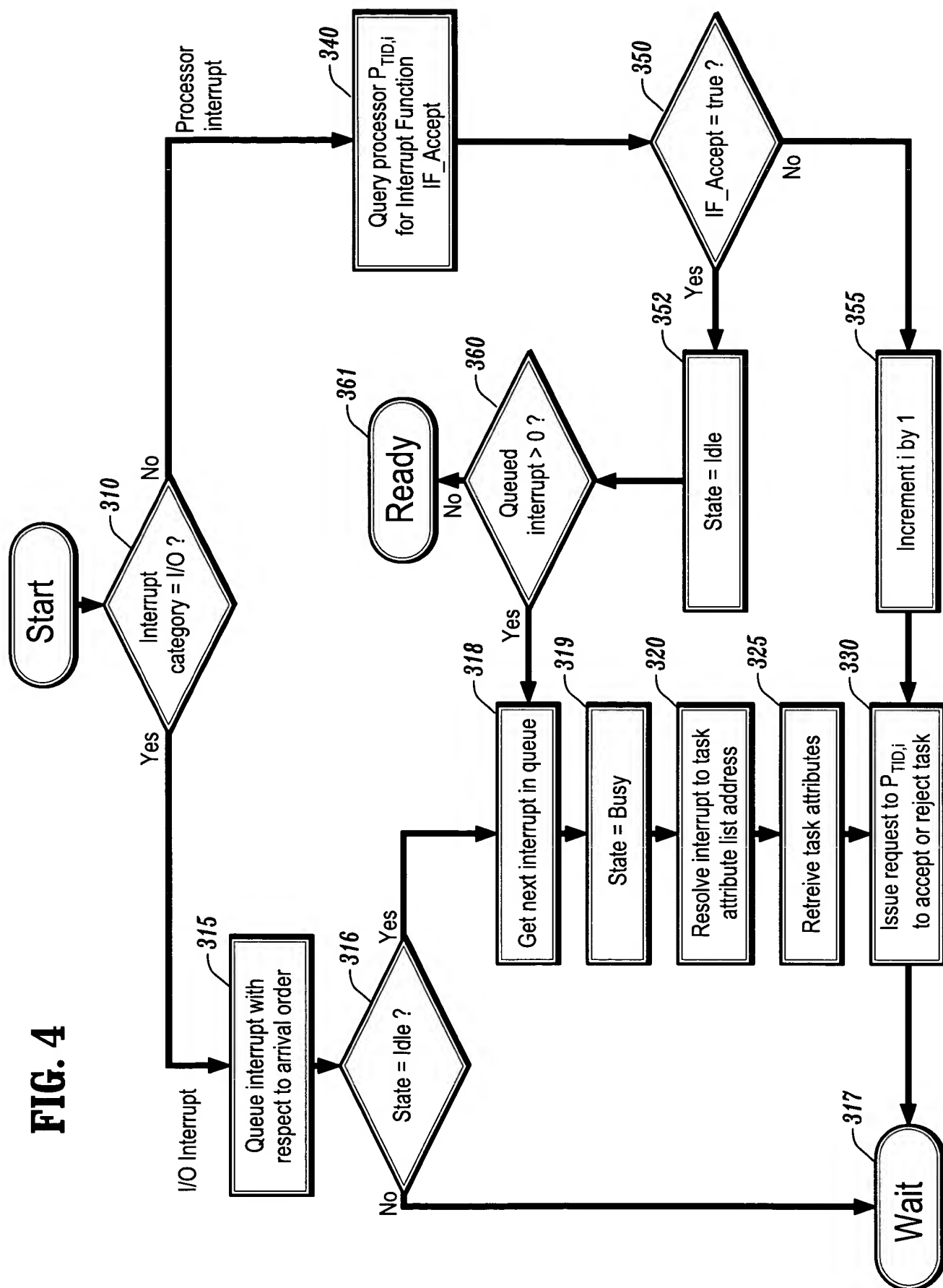
**FIG. 1**

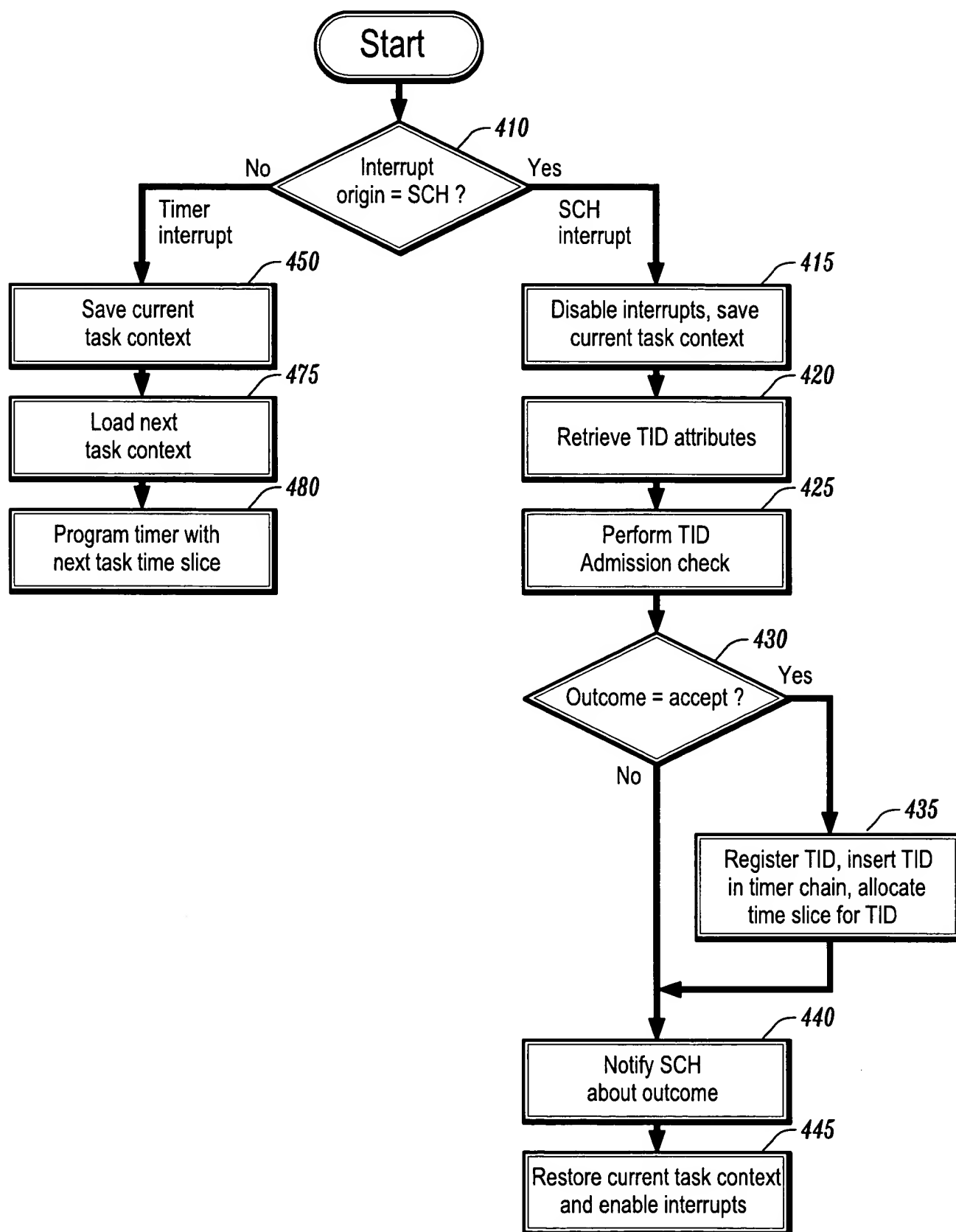
<i>Parameter</i>	<i>Type</i>	<i>Unit</i>	<i>Description</i>
TID	word	n/a	Task IDentification number
NP	byte	n/a	Number of potential host processing units
P <sub>TID,1</sub>	byte	n/a	ID of most energy efficient processor
CPS <sub>TID,P<sub>TID,1</sub></sub>	word	[kHz]	Required cycles/sec to sustain task
ADDR <sub>1</sub>	void*	n/a	Pointer to task code
.			
.			
.			
P <sub>TID,NP</sub>	byte	n/a	ID of least energy efficient processor
CPS <sub>TID,P<sub>TID,NP</sub></sub>	word	[kHz]	Required cycles/sec to sustain task
ADDR <sub>NP</sub>	void*	n/a	Pointer to task code

**FIG. 2**

<i>Parameter</i>	<i>Type</i>	<i>Unit</i>	<i>Description</i>
N	byte	n/a	Number of processing units
CPS <sub>1</sub>	word	[kHz]	Currently available cycles/sec
.			
.			
.			
CPS <sub>NP</sub>	word	[kHz]	Currently available cycles/sec

**FIG. 3**



**FIG. 5**